

ABSTRACT OF THE INVENTION

A bus interface extender and a method thereof, is utilized between a bus arbitrator (e.g. a PCI bus arbitrator) and at least one PCI device, such that the amount of bus devices electrically coupled with the bus arbitrator can be increased without modifying architecture of the bus arbitrator. The bus arbitrator has a priority decision module, a grant decision module, and a bus signal processing module. The priority decision module determines a priority sequence for each of the bus devices. The grant decision module grants access of the proper bus device to the system bus, according to request status and priority thereof. The bus signal processing module manages transmission of request/grant signals among the PCI devices and the bus arbitrator, according to decision of the grant decision module.

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